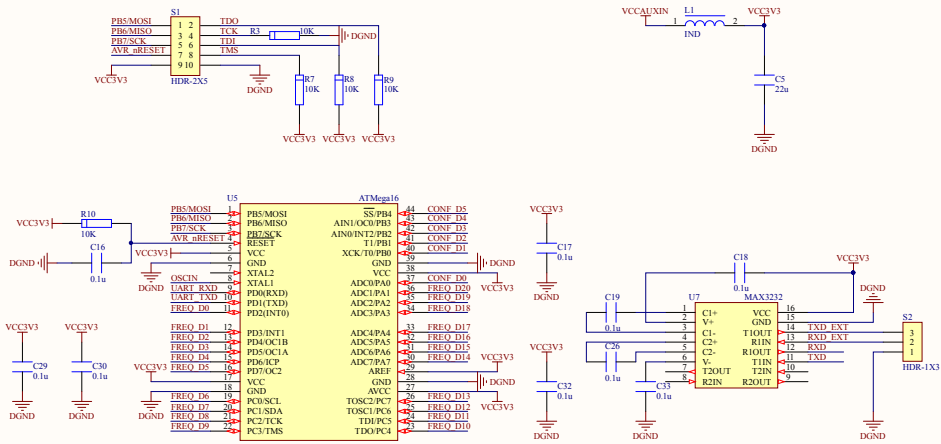


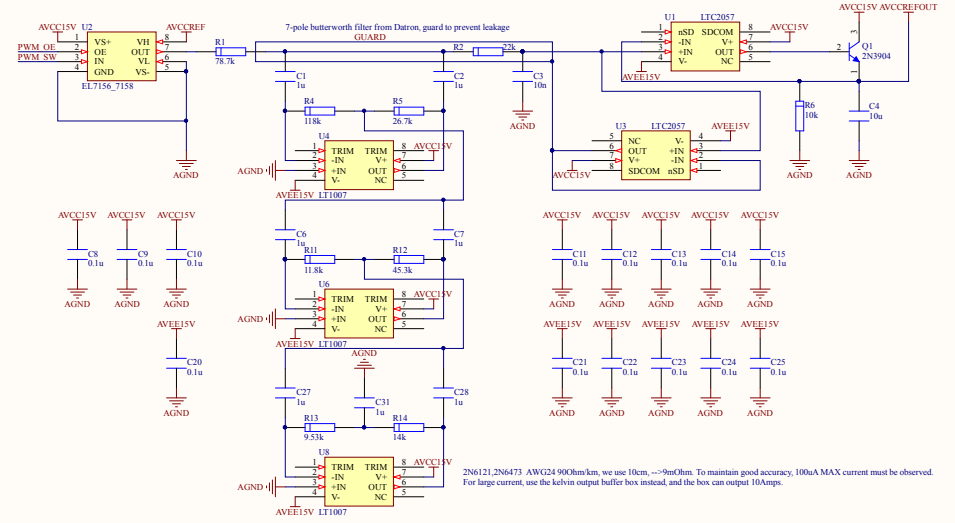
Digital subsystem

AVR microcontroller

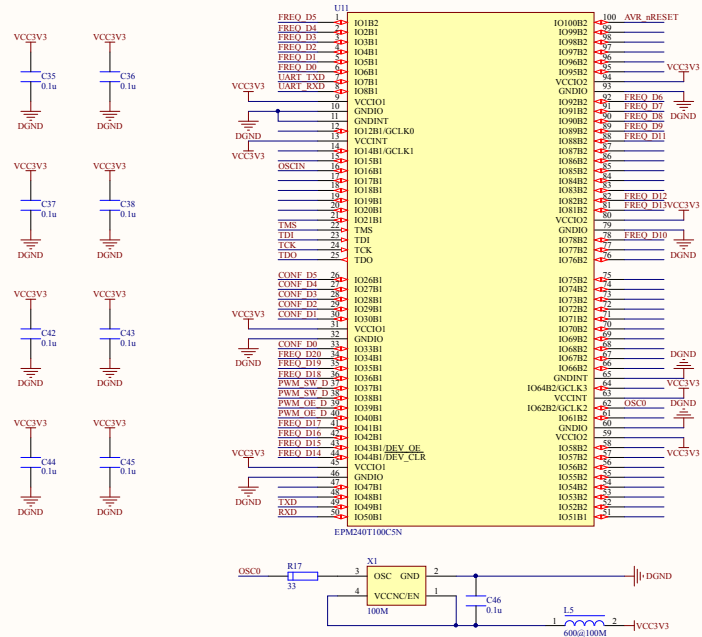


Analog subsystem

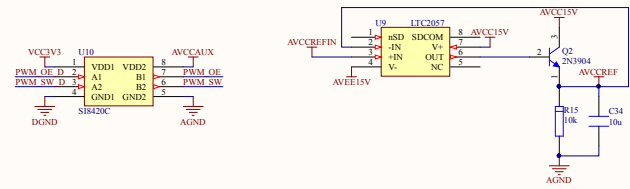
Main PWM



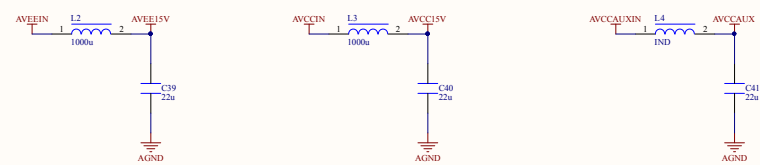
EPM240 CPLD



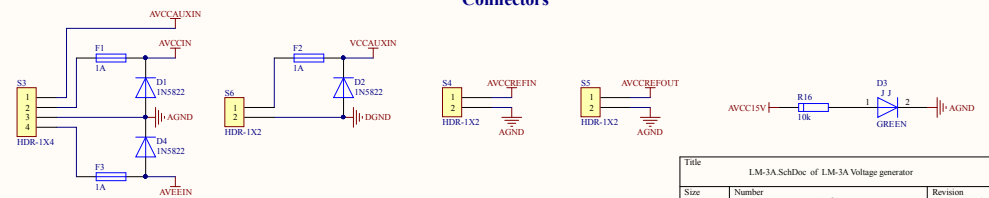
PWM isolator/Reference buffering



Power processing stage



Connectors



Title			
LM-3A.SchDoc of LM-3A Voltage generator			
Size	Number	Revision	
A2	1 of 1	r2p0	
Date:	2015/5/14	Sheet of LM-3A Voltage generator	
File:	F:\Code Library\3.M-3A_AnalogSW\SchDoc	Drawn By: Pvy	

Board s